## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Ting, et al.

Docket No.:

TSM03-0945

Serial No.:

10/840,125

Art Unit:

2811

Filed:

May 6, 2004

Examiner:

Nadav, Ori

For:

Notched Spacer for CMOS Transistors

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT**

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's Office Action mailed March 26, 2008 and the Notice of Non-Compliant Amendment (37 C.F.R. § 1.121) mailed August 11, 2008. Please amend the above-referenced application as follows.